

ACL Enforcement Overhead by ISA and TTL Policy

Log..... scale. Baseline: amd64 = 261,098 ticks; aarch64/riscv64 = 261,095 ticks. 30 reps per cell.
Floor = 256 partial (2 seeds amd64, 1 seed each RISC). ACL-RWT: all 9 cells confirmed.

Overhead vs. no-ACL baseline (% , log..... scale)

